

GENERAL DESCRIPTION

CM11.6V is designed to boost standby efficiency. It is so called "Efficiency Vitamin " . CM11.6V can supply up to 2A with 12V output.

The CM11.6V is designed to work with standby 11.1V while the main output of power supply is +12V. The CM11.6V is not activated until both "main 12V (input) " reaches 11.1V and "Standby 11.1V (output) " reaches 11.1V; then CM11.6V connects between main 12V (input) and standby 11.1V (output). It forces Standby = 12V and (Iload from standby current) x (Rdson of the switch inside of CM11.6V) ~ 11.7V.

The CM11.6V has two current limits + a ~20% duty and ~ 12mS re-try timer.

The CM11.6V series is available in SOP8 packages. A minimum of 10uF ceramic or Electronic capacitor is required at the input and output to ensure the high performance.

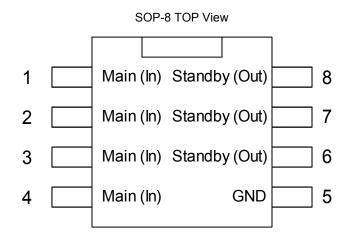
FEATURES

- No diode between input and output (Avoid reverse current)
- Patents Pending
- Efficiency Vitamin and it boost total efficiency at 20%, 50% and 100% load
- Low Rdson ~ 0.15 ohm (room temp.)
- Current Limiting and Thermal Protection to handle rugged application environments
- Short Circuit Protection
- It can handle current up to 2A (for 3A, talk to CMC FAEs)
- Standby-Main Intrusion Protection
- ► Temperature Range -40°C to 125°C
- SOP-8 package available

APPLICATIONS

Work with Standby Output > 11.1V applications

PIN CONFIGURATION





ORDERING INFORMATION

Part Number	Operating Temperature Range (T _A)	Package
CM11.6VIS	-40°C ~+125°C	SOP-8
CM11.6VISTR*	-40°C ~+125°C	SOP-8

*Note: TR : Package is Typing Reel

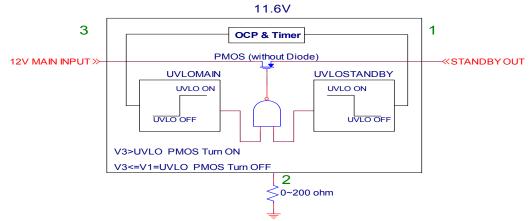
ABSOLUTE MAXIMUM RATINGS

Input Voltage	+18V
Output Voltage	+17.98V
Peak current accept trough 11.6V	20A/5ms
Peak current accept trough 11.6V	10A/10ms
Operating Junction Temperature Range, T_J	40℃ to +125℃
Storage Temperature	65℃ to +150℃
Lead Temperature	
Metal package (Soldering ,10 sec.)	300 °C
Plastic package (Soldering ,4 sec.)	260 °C
Maximum surface Temperature	110 ℃
Internal Over Current Protection (T_J=25 $^\circ\!\!\!\mathrm{C}$)	2.8A

THERMAL INFORMATION

Parameter	Package	Maximum	Unit
Thermal Resistance (θ_{jc})	SOP8	23.5	°C/W
Thermal Resistance (θ_{ja}) (No heat sink)	SOP8	140	°C/W
Internal Power Dissipation (P _D) (ΔT = 100 $^{\circ}$ C, No Heat sink)	SOP8	900	mW
Maximum Junction Temperature		150	°C
Maximum Lead Temperature (10 Sec)		300	°C
Maximum surface Temperature		110	°C

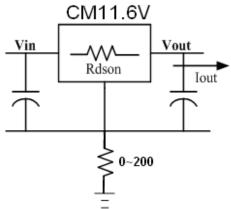
BLOCK DIAGRAM





ELECTRICAL CHARACTERISTICS

Electrical Characteristics at I_{OUT} = 10mA, and T_J = +25 $^\circ\!{\rm C}$; unless otherwise noted Test Circuit:



Symbol	Parameter	Conditions	Min	Тур	Мах	Units
lccq	CM11.6V quiescent current	GND Pin Current (On state)			70	uA
		Vin = 12V ;I _{OUT} =0.1A ; T _J =25℃	11.98	11.99	12.0	V
	Vout Voltage	Vin = 12V ;I _{OUT} =0.5A ; T _J =25℃	11.915	11.93	11.945	V
V _{OUT}	(Note 1,2,3)	Vin = 12V ;I _{OUT} =1.5A ; TJ=25℃	11.73	11.78	11.82	V
		Vin = 12V ;I _{OUT} =2A ; T _J =25℃	11.62	11.68	11.74	V
UVLO	Under voltage Lockout Threshold		10.9	11.05	11.1	V
OCP Internal O.C.P test		Vin = 12.7V ; TJ=-20℃ Vin = 14V ; TJ=-20℃		3		А
	Vin = 12.7V ; TJ=25℃ Vin = 14V ; TJ=25℃		2.7		А	
		Vin = 12.7V ; TJ=50℃ Vin = 14V ; TJ=50℃		2.6		А
	Thermal Resistance Junction-to-Case	SOP8		23.5		°C/W
	Thermal Resistance Junction-to-Ambient (No heat sink; No air flow)	SOP8		140		°C/W

Note 1: Typical Values represent the most likely parametric norm.

Note 2: All limits are guaranteed by testing or statistical analysis.

Note 3: Rds-on=(Vin-Vout) / Iout.

Note 4: Minimum pad size of 0.038in²



THERMAL DATA:

		Product:11	.6V(SOP8)			
Ambient Temp.	Main voltage=12.7V	I Load(A)	PKG Temp.(C) Burn in 2 Hours	Rdson(ohm)		
2 5°C	12.7	0.5	27	0.139		
25°C	12.7	1	31	0.148		
25°C	12.7	1.5	41	0.157		
25°C	12.7	2	55	0.171		
25°C	12.7	2.3	68	0.184		
25°C	12.7	2.5	82	0.187		
	Product:11.6V(SOP8)					
Ambient Temp.	Main voltage=12.7V	I Load(A)	PKG Temp.(C) Burn in 2 Hours	Rdson(ohm)		
50°C	12.7	0.5	51	0.155		
50°C	12.7	1	53	0.159		
50°C	12.7	1.5	59	0.167		
50°C	12.7	2	69	0.180		
50°C	12.7	2.3	76	0.188		
50°C	12.7	2.5	94	0.195		
		Product:11	.6V(SOP8)			
Ambient Temp.	Main voltage=12.7V	I Load(A)	PKG Temp.(C) Burn in 2 Hours	Rdson(ohm)		
-20°C	12.7	0.5	-19	0.127		
-20°C	12.7	1	-17	0.129		
-20°C	12.7	1.5	-8	0.135		
-20°C	12.7	2	0	0.145		
-20°C	12.7	2.3	6	0.154		
-20°C	12.7	2.5	10	0.161		



APPLICATION NOTE

Like any linear circuit, CM11.6V requires external capacitors to ensure stability and high performance.

Input By Pass Capacitor

An input capacitor of at least 10μ F is required. Ceramic or Electronic capacitor can be used. The value can be increase without upper limit.

A Ceramic or Electronic capacitor on the input is a suitable input bypassing for almost all applications

Output Capacitor

An output capacitor is required for high performance. The minimum value is 10μ F but may be increase without limit.

GND resistor (Rg) 0~200 ohm

Add Rg (0~200 ohm) resistor in series with GND will improve immunity against abnormal condition such as lighting surge, ESD.

No Diode between input and output

CM11.6V is not like switch MOS. There is no diode between input and output. Therefore, either input is shorted or output is shorted will not effect each other.

Two Current limit:

UVLO protect:

The CM11.6V has two current limit U.V.L.O and internal thermal shutdown to protect the device from over-heating.

The first Current limit = (Vin-11.1V(U.V.L.O)) / Rdson

The second Current limit ~ 0.45V/Rdson~3A(Room temp.)

Internal OCP Protect:

25mS ~ 20ms off + ~5mS on, Retry Timer:

When the internal current limit protecting circuitry is triggered, the retry timer and protection logic turn off the switch until 20mS timer finished. After that, it allows ~5mS to retry.

Thermal Considerations

It is important that the thermal limit of the package is not exceeded. The CM11.6V has built-in thermal protection. When the thermal limit is exceeded, the IC will enter protection. The power dissipation for a given application can be calculated as following: The power dissipation (P_D) is

$$\begin{split} \mathsf{P}_{\mathsf{D}} &= \mathsf{I}_{\mathsf{OUT}} * [\mathsf{V}_{\mathsf{IN}} - \mathsf{V}_{\mathsf{OUT}}] \\ \mathsf{I}_{\mathsf{IN}} &= \mathsf{I}_{\mathsf{L}} + \mathsf{I}_{\mathsf{G}} \\ \mathsf{P}_{\mathsf{D}} &= (\mathsf{V}_{\mathsf{IN}} - \mathsf{V}_{\mathsf{OUT}}) \; \mathsf{I}_{\mathsf{L}} + \mathsf{V}_{\mathsf{IN}} \mathsf{I}_{\mathsf{G}} \end{split}$$

Figure 1 shows the voltages and currents which are present in the circuit.

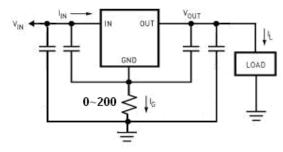


FIGURE 1. Power Dissipation Diagram

Heat-sink May Required

When an integrated circuit operates with an appreciable current, its junction temperature is elevated. It is important to quantify its thermal limits in order to achieve acceptable performance and reliability. This limit is determined by summing the individual parts consisting of a series of temperature rises from the semiconductor junction to the operating environment. A one-dimensional steady-state model of conduction heat transfer is demonstrated if Figure 7. The heat generated at the device junction flows through

the die to the die attach pad, through the lead frame to the surrounding case material, to the printed circuit board, and eventually to the ambient environment. Below is a list of variables that may affect the thermal resistance and in turn the need for a heat-sink.

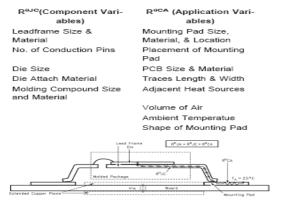
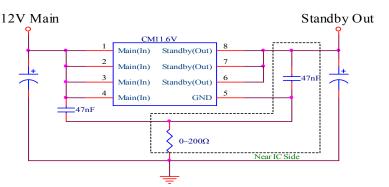


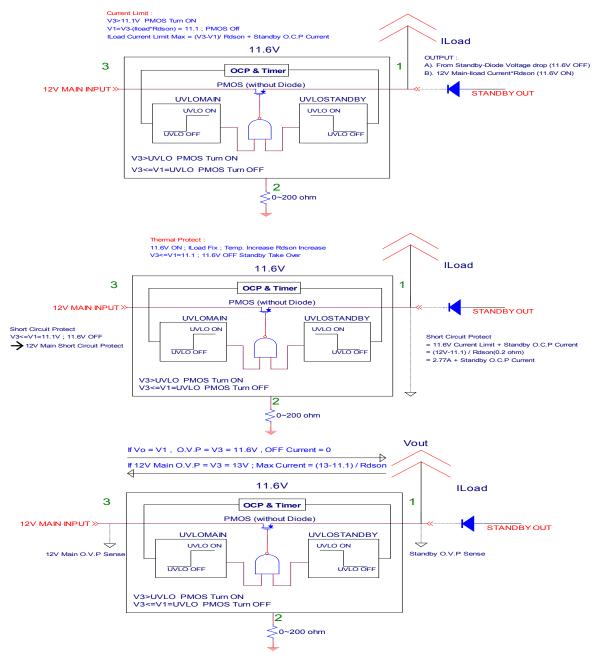
FIGURE 2. Cross-sectional view of Integrated Circuit Mounted on a printed circuit board. Note that the case temperature is measured at the point where the leads contact with the mounting pad surface



TYPICAL APPLICATION CIRCUIT

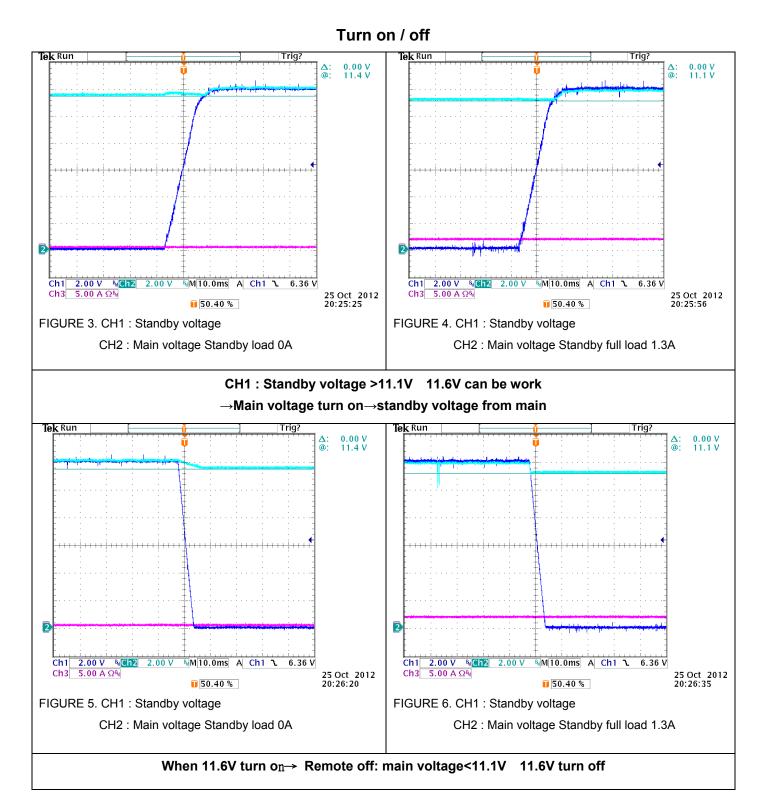


PROTECTION: Internal O.C.P/Short-circuit/U.V.L.O current limit





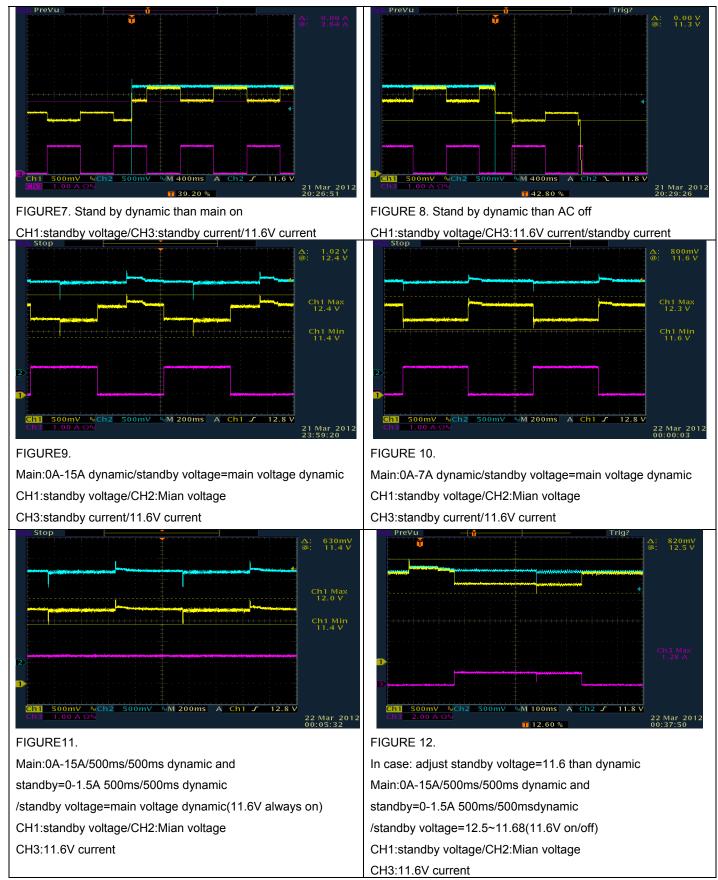
WAVEFORM TEST





CM11.6V RUGGED 2A SWITCH; EFFICIENCY VITAMIN TO BOOST STANDBY EFFICIENCY NO DIODE BETWEEN INPUT AND OUTPUT WORK WITH STANDBY > 11.1V

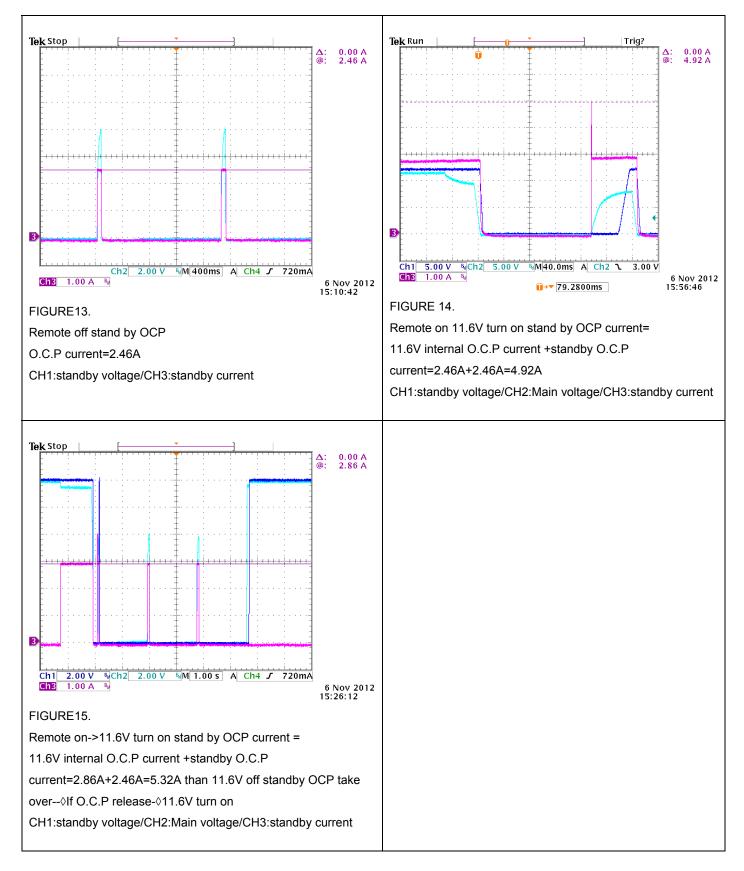
Dynamic





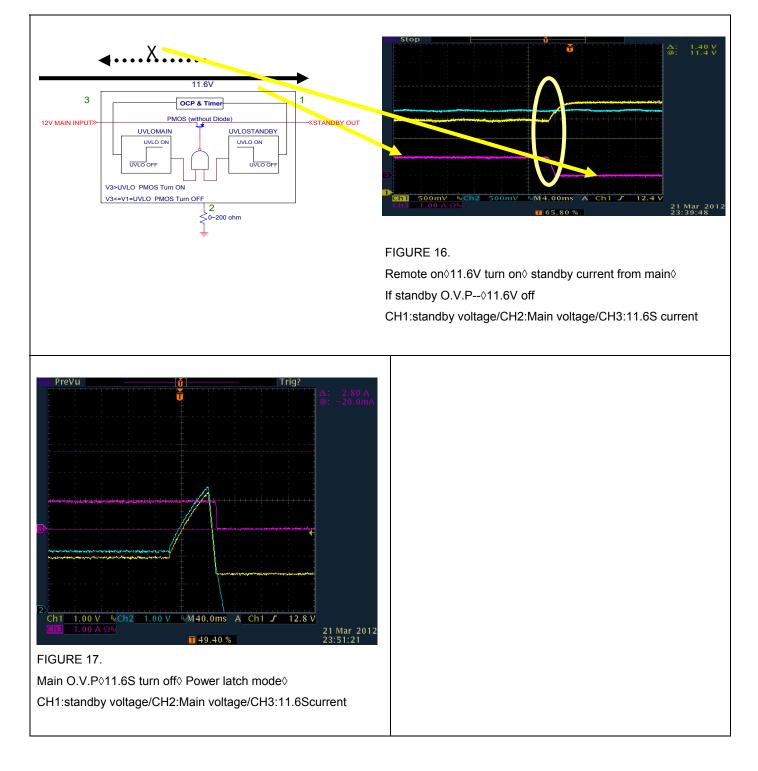
RUGGED 2A SWITCH; EFFICIENCY VITAMIN TO BOOST STANDBY EFFICIENCY NO DIODE BETWEEN INPUT AND OUTPUT WORK WITH STANDBY > 11.1V

O.C.P. Current



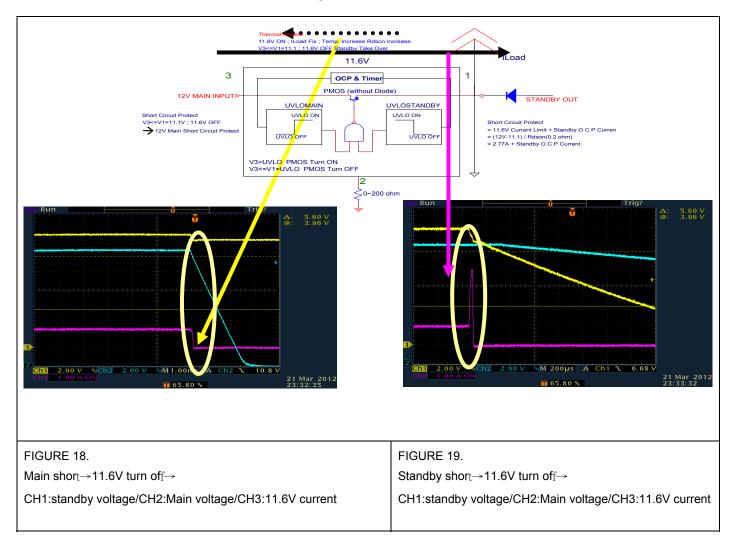


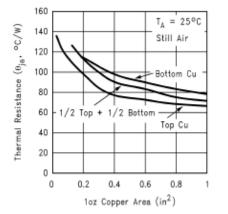
O.V.P. Current

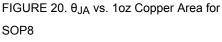


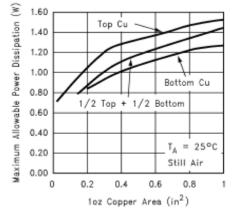


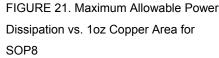
Output Short Circuit











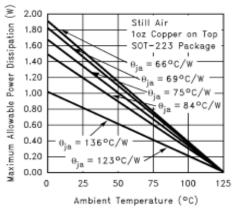
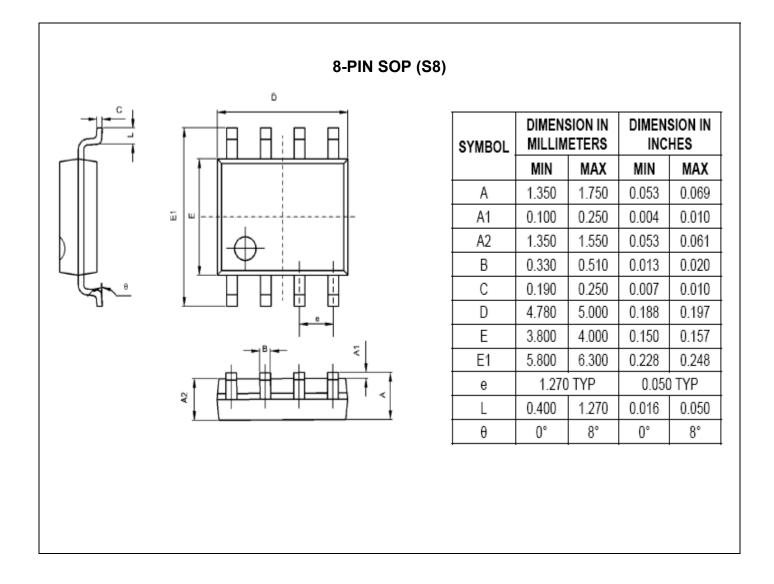


FIGURE 22. Maximum Allowable Power Dissipation vs. Ambient Temperature for Sop8



PACKAGE DIMENSION





IMPORTANT NOTICE

Champion Microelectronic Corporation (CMC) reserves the right to make changes to its products or to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

A few applications using integrated circuit products may involve potential risks of death, personal injury, or severe property or environmental damage. CMC integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life-support applications, devices or systems or other critical applications. Use of CMC products in such applications is understood to be fully at the risk of the customer. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.

CMC assumes to no liability to customer product design or application support. CMC warrants the performance of its products to the specifications applicable at the time of sale.

HsinChu Headquarter	Sales & Marketing		
5F, No. 11, Park Avenue II,	21F., No. 96, Sec. 1, Sintai 5th Rd., Sijhih City, Taipei County 22102,		
Science-Based Industrial Park, HsinChu City, Taiwan			
	Taiwan, R.O.C.		
TEL:+886-3-5679979	TEL: +886-2-2696 3558		
FAX: +886-3-5679909	FAX: +886-2-2696 3559		
FAX: +886-3-5679909	FAX: +886-2-2696 3559		

. . . .