

# UNISONIC TECHNOLOGIES CO., LTD

12NM90 **Preliminary Power MOSFET** 

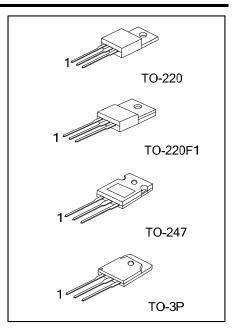
# 12A, 900V N-CHANNEL SUPER-JUNCTION MOSFET

#### DESCRIPTION

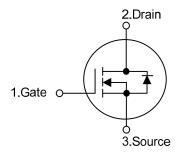
The UTC 12NM90 is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at DC-DC, AC-DC converters for power applications.

#### **FEATURES**

- \*  $R_{DS(ON)}$  < 0.70 @  $V_{GS}$  = 10V,  $I_{D}$  = 6.0A
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness



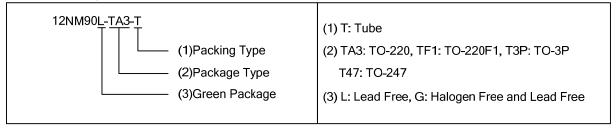
#### **SYMBOL**



### **ORDERING INFORMATION**

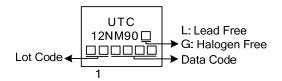
Ordering Number		Dackago	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
12NM90L-TA3-T	12NM90G-TA3-T	TO-220	G	D	S	Tube	
12NM90L-TF1-T	12NM90G-TF1-T	TO-220F1	G	D	S	Tube	
12NM90L-T47-T	12NM90G-T47-T	TO-247	G	D	S	Tube	
12NM90L-T3P-T	12NM90G-T3P-T	TO-3P	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



www.unisonic.com.tw 1 of 7

# **■** MARKING



## ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	900	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Continuous Drain Current	Continuous	I <sub>D</sub>	12	Α
Pulsed Drain Current	Pulsed (Note 2)	I <sub>DM</sub>	48	Α
Avalanche Current (Note 2)		I <sub>AR</sub>	2.8	Α
Single Pulsed Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	623	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.4	V/ns
	TO-220		180	W
[ ]	TO-220F1	_	51	W
Power Dissipation	TO-3P	P <sub>D</sub> -	390	W
[	TO-247		360	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 159mH,  $I_{AS}$  = 2.8A,  $V_{DD}$  = 50V,  $R_G$  = 25 $\Omega$ , Starting  $T_J$  = 25 $^{\circ}$ C.
- 4.  $I_{SD} \le 12A$ , di/dt  $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25$ °C.

#### **■ THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F1		62.5	°C/W
	TO-3P	$\theta_{JA}$	30	°C/W
	TO-247		40	°C/W
Junction to Case	TO-220		0.69	°C/W
	TO-220F1	Δ	2.45	°C/W
	TO-3P	$\theta_{JC}$	0.32	°C/W
	TO-247		0.35	°C/W

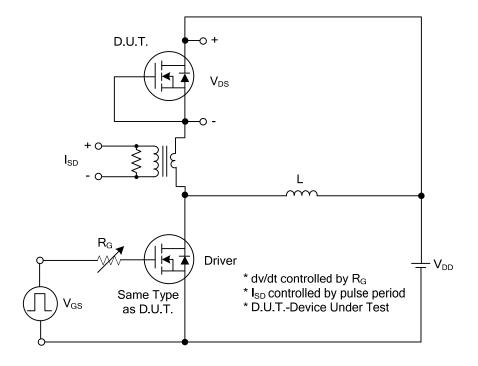
# ■ ELECTRICAL CHARACTERISTICS (T」=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	900			V
Drain-Source Leakage Current		I <sub>DSS</sub>	$V_{DS} = 900V, V_{GS} = 0V$			10	μΑ
Gate-Source Leakage Current	Forward		$V_{GS} = 30V, V_{DS} = 0V$			100	nΑ
	Reverse	$I_{GSS}$	$V_{GS} = -30V, V_{DS} = 0V$			-100	nΑ
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	$V_{GS} = 10V, I_D = 6.0A$			0.7	Ω
DYNAMIC CHARACTERISTICS	_						
Input Capacitance	put Capacitance				1230		pF
Output Capacitance		$C_{OSS}$	$V_{GS}$ =0V, $V_{DS}$ =25V, f=1.0MHz		425		pF
Reverse Transfer Capacitance		$C_{RSS}$			9		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		$Q_{G}$	  V <sub>DS</sub> =50V, I <sub>D</sub> =1.3A, I <sub>G</sub> =100μA		65		nC
Gate to Source Charge		$Q_GS$	V <sub>GS</sub> =10V (Note 1,2)		6		nC
Gate to Drain Charge		$Q_GD$	VGS=10V (Note 1,2)		21		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			58		nS
Rise Time		$t_R$	$V_{DD} = 30V$ , $I_D = 0.5A$ , $R_G = 25\Omega$ ,		125		nS
Turn-OFF Delay Time		$t_{D(OFF)}$	V <sub>GS</sub> =10V (Note 1,2)		500		nS
Fall-Time		t <sub>F</sub>			135		nS
SOURCE- DRAIN DIODE RATIN	NGS AND CH	ARACTERIST	rics				
Maximum Body-Diode Continuous Current		Is				12	Α
Maximum Body-Diode Pulsed Current		$I_{SM}$				48	Α
Drain-Source Diode Forward Voltage (Note 1)		$V_{SD}$	I <sub>S</sub> =12A, V <sub>GS</sub> =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		t <sub>rr</sub>	I <sub>S</sub> =12A, V <sub>GS</sub> =0V,		595		nS
Body Diode Reverse Recovery Charge		$Q_{rr}$	dI <sub>F</sub> /dt=100A/μs		10.4		μC

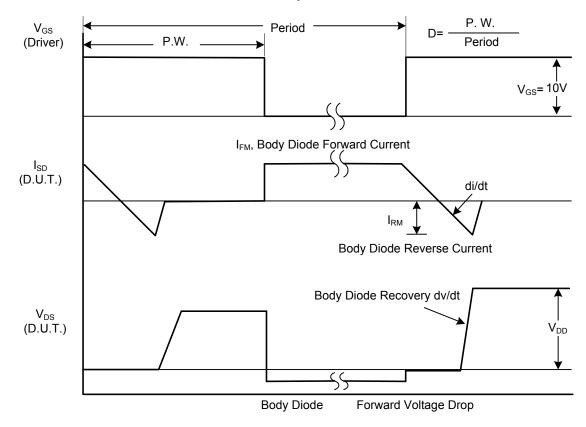
Notes: 1. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2%.

<sup>2.</sup> Essentially independent of operating ambient temperature.

#### **■ TEST CIRCUITS AND WAVEFORMS**

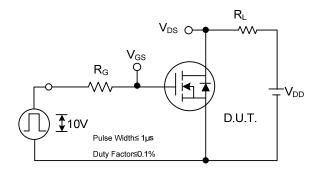


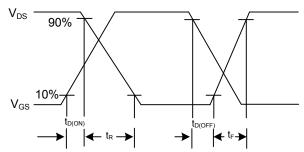
# Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

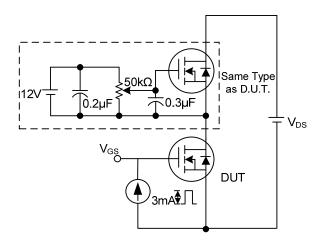
# ■ TEST CIRCUITS AND WAVEFORMS (Cont.)

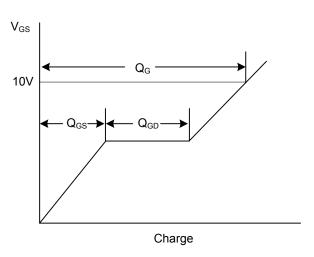




**Switching Test Circuit** 

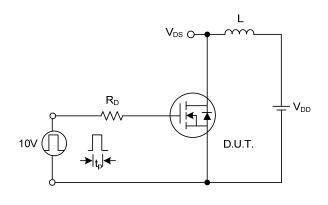
**Switching Waveforms** 

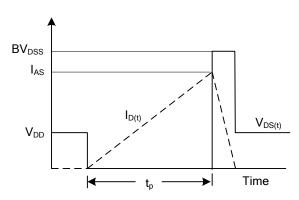




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.

