



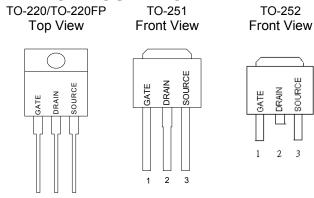
GENERAL DESCRIPTION

This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This hew high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits. ◆

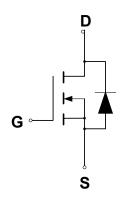
FEATURES

- ◆ SJ MOS
- Higher Current Rating
- ◆ Lower Rds(on)
- ◆ Lower Capacitances
- ◆ Lower Total Gate Charge

PIN CONFIGURATION



SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

Rating			Value	Unit
Drain to Current — Continuous		I _{D(1)}	7.0	Α
- Pulsed		I _{DM}	21.0	
Gate-to-Source Voltage — Continue		V_{GS}	±20	V
Total Power Dissipation TO251/TO252		P _D	81.2	W
	TO-220		85.0	
	TO-220FP		27	
Derate above 25℃	TO251/TO252		0.65	W/°C
	TO220		0.68	
	TO220FP		0.22	
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 150	$^{\circ}\!\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-$ T _J = 25 $^{\circ}$ C		_		_
$(V_{DD} = 100V, V_{GS} = 10V, I_{L} = 3.3A, L = 10mH, R_{G} = 25\Omega)$		E _{AS}	54.5	mJ
Thermal Resistance — Junction to Case TO251/TO252		θ _{JC}	1.54	°CW
	TO-220		1.47	
TO220FP			4.56	
 Junction to Ambient TO251/TO252/ TO-220/ TO-220FP 		θ _{JA}	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		TL	260	$^{\circ}\!\mathbb{C}$

(1)Drain current limited by maximum junction temperature (TO220)





ORDERING INFORMATION

Part Number	TOP MARK	Part Number	Packing Mthod	Note
GP07S65XN251 (Note1)	GP07S65X	TO-251	Tube	
GP07S65XN252 (Note1)	GP07S65X	TO-252	Tube	
GP07S65XN252TR (Note1)	GP07S65X	TO-252	Tape and Reel	
GP07S65XN220 (Note1)	GP07S65X	TO-220	Tube	
GP07S65XN220FP (Notte1)	GP07S65X	TO-220FP	Tube	

Note1: X: Suffix for Halogen Free and PB Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^{\circ}C$.

				GP07S65		
Chara	Symbol	Min	Тур	Max	Units	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	650			V	
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current	I _{DSS}			1	uA	
$(V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Ford	I _{GSSF}			100	nA	
$(V_{gsf} = 20 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Rev	I _{GSSR}			100	nA	
$(V_{gsr} = -20 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage	$V_{GS(th)}$	2	3	4	V	
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$						
Static Drain-Source On-Resistance	R _{DS(on)}			0.7	Ω	
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	C _{iss}		521.7		pF
Output Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz)	Coss		261.3		pF
Reverse Transfer Capacitance		C _{rss}		48.0		pF
Turn-On Delay Time	()/ = 250 \/ = 7 A	$t_{d(on)}$		10.6		ns
Rise Time	$(V_{DD} = 250 \text{ V}, I_D = 7 \text{ A},$ $V_{GS} = 10 \text{ V},$	t _r		37.2		ns
Turn-Off Delay Time	$V_{GS} = 10 \text{ V},$ $R_{G} = 9.1\Omega) *$	$t_{d(off)}$		53.6		ns
Fall Time	NG - 3.122)	t _f		51.2		ns
Total Gate Charge	()/ - 400 \/ \ \ - 70	Qg		15.7		nC
Gate-Source Charge	$(V_{DS} = 400 \text{ V}, I_{D} = 7\text{A}, V_{GS} = 10 \text{ V})^*$	Q _{gs}		3.4		nC
Gate-Drain Charge	VGS - 10 V)	Q_gd		7.5		nC
SOURCE-DRAIN DIODE CHARAC	TERISTICS					
Forward On-Voltage(1)		V _{SD}			1.5	V
Forward Turn-On Time	$(I_S = 7A,$ $d_{IS}/d_t = 100A/us)$	t _{on}		**		ns
Reverse Recovery Time	$u_{ S}/u_t - 100A/\mu S$	t _{rr}		232		ns

^{*} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

^{**} Negligible, Dominated by circuit inductance





TYPICAL ELECTRICAL CHARACTERISTICS

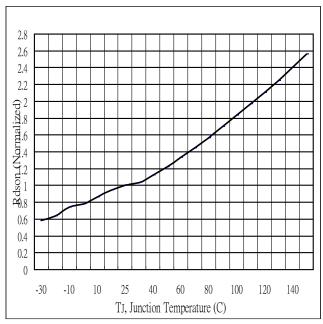
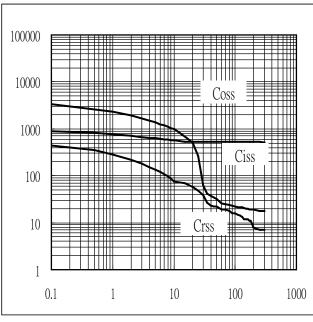


Fig 1. On-Resistance Variation with vs. **Temperature**



Typical Capacitance Vs. **5.** Fig **Drain-to-Source Voltage**

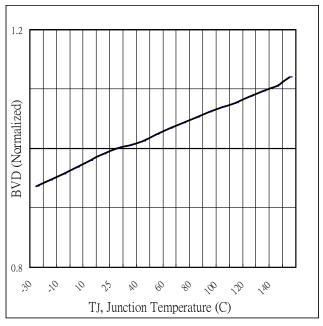


Fig.2 Breakdown Voltage Variation vs. **Temperature**

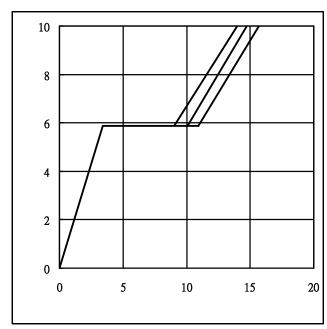
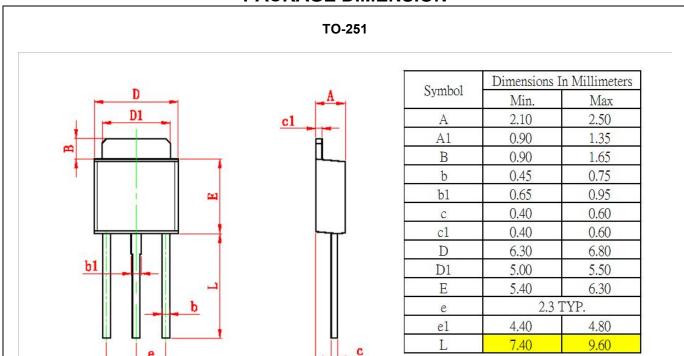


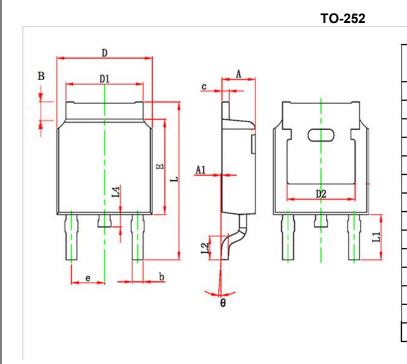
Fig Typical Gate Charge **6. Gate-to-Source Voltage**





PACKAGE DIMENSION



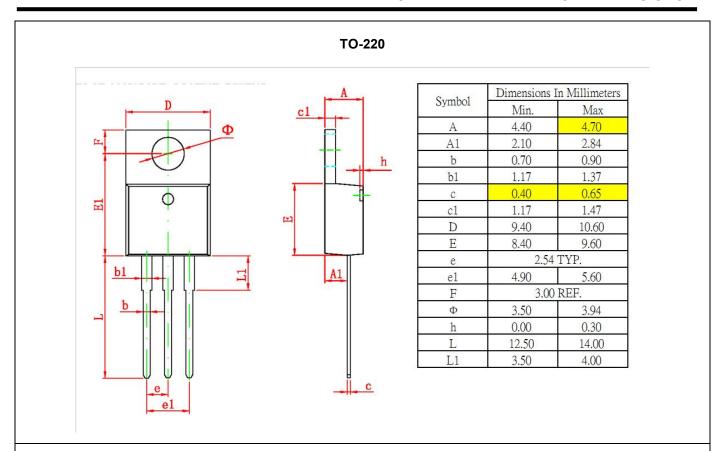


Symbol	Dimensions In Millimeters			
Symbol	Min.	Max		
A	2.10	2.50		
A1	0.90	1.35		
В	0.90	1.65		
b	0.45	0.90		
С	0.40	0.60		
D	6.30	6.80		
D1	5.00	5.50		
D2	4.83 TYP.			
Е	5.90	6.30		
е	2.3 TYP.			
L	9.30	10.50		
L2	1.20	1.80		
L4	0.60	1.00		
θ	0.00	10.00		

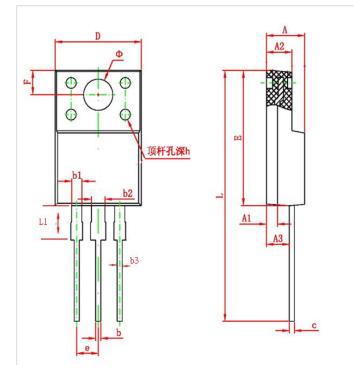




Power Field Effect Transistor







C11	Dimensions In Millimeters			
Symbol	Min.	Max		
A	4.50	4.90		
A1	1.3 REF.			
A2	2.20	3.20		
A3	2.10	3.20		
Ъ	0.70	0.90		
b1	1.00	1.75		
b2	1.00	1.75		
b3	0.50	0.80		
С	0.45	0.65		
D	9.90	10.40		
Е	14.60	16.20		
е	2.54 TYP.			
F	3.00 REF.			
Φ	3.08	3.30		
h	0.00	0.30		
L	28.00 30.00			
L1	3.20	3.55		





IMPORTANT NOTICE

Great Power Microelectronic Corporation (GP) reserves the right to make changes to its products or to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

A few applications using integrated circuit products may involve potential risks of death, personal injury, or severe property or environmental damage. GP integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life-support applications, devices or systems or other critical applications. Use of GP products in such applications is understood to be fully at the risk of the customer. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.



虹冠電子工業股份有限公司 Champion Microelectronic Corporation Web:http://www.champion-micro.com/



深圳市冠顺微电子股份有限公司 Shenzhen Great Power Co., Ltd Web:http:// www.greatpowermicro.com

新北市汐止區新台五路一段 96 號 21F

21F., No. 96, Sec. 1, Sintai 5th Rd., Sijhih City, Taipei County 22102,

Taiwan, R.O.C.

TEL: +886-2-2696 3558 FAX: +886-2-2696 3559 深圳市福田区深南大道 7002 号财富广场 A座 4V,

4V, Tower A, Fortune Plaza, No. 7002, Shennan Road, Futian District, Shenzhen City, China

PC: 518040

TEL: +86-755-83709176 FAX: +86-755-83709276