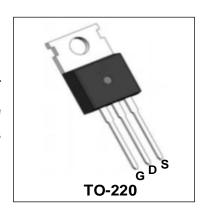


### **60V N-Channel Enhancement Mode Power MOSFET**

# **Description**

WMK025N06HG2 uses Wayon's 2<sup>nd</sup> generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching performance.



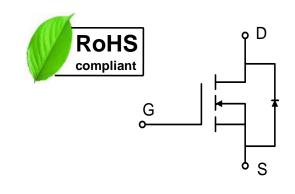
#### **Features**

- $V_{DS}$ = 60V,  $I_D$  = 200A  $R_{DS(on)}$  < 3.2m $\Omega$  @  $V_{GS}$  = 10V
- Low R<sub>DS(ON)</sub>
- Low Gate Charge
- 100% EAS Guaranteed

# **Applications**

- Synchronous Rectification
- DC/DC Converter

# **Absolute Maximum Ratings**



Parameter		Symbol	Value	Unit	
Drain-Source voltage		V <sub>DS</sub>	60	V	
Gate-Source voltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current <sup>1</sup>	Tc=25°C	- I <sub>D</sub>	200	^	
	Tc=100°C		106	Α	
Pulsed Drain Current <sup>2</sup>		Ірм	580	Α	
Single Pulse Avalanche Energy <sup>3</sup>		EAS	605	mJ	
Avalanche Current		las	55	Α	
Total Power Dissipation <sup>4</sup>	T <sub>C</sub> =25°C	P <sub>D</sub>	245	W	
Operating Junction and Storage Temperature Range		Тл, Тетс	-55 to 150	°C	

#### **Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>1</sup>	Reja	32	°C/W
Thermal Resistance from Junction-to-Case <sup>1</sup>	R <sub>θ</sub> JC	0.51	°C/W



### Electrical Characteristics T<sub>c</sub> = 25°C, unless otherwise noted

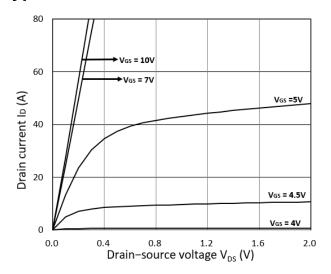
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static Characteristics	1				l	I
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	60	-	-	V
Gate-Body Leakage current	lgss	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	0.1	μΑ
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3	4	V
Drain-Source on-Resistance <sup>2</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	2.5	3.2	mΩ
Dynamic Characteristics	<u>.</u>					
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V, f =1MHz	-	4683	-	pF
Output Capacitance	Coss		-	1192	-	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	-	69	-	
Switching Characteristics	<u>.</u>					
Gate Resistance	Rg	$V_{DS}$ =0V , $V_{GS}$ =0V , $f$ =1MHz	-	2	-	Ω
Total Gate Charge	Qg	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> =50A	-	96	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	19.5	-	
Gate-Drain Charge	$\mathbf{Q}_{gd}$		-	12.1	-	
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{GS} = 10V, V_{DS} = 30V,$ $R_{G} = 2\Omega, I_{D} = 25A$	-	20.8	-	
Rise Time	tr		-	5.2	-	nS
Turn-off Delay Time	t <sub>d(off)</sub>		-	78.8	-	. 113
Fall Time	t <sub>f</sub>		-	24.9	-	
Drain-Source Body Diode Chara	cteristics					
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1A, V <sub>GS</sub> = 0V	-	-	1	V
Continuous Source Current <sup>1,5</sup>	Is	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	-	-	200	Α

#### Notes:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%
- 3. The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}$ =40V,  $V_{\text{GS}}$ =10V, L=0.4mH,  $I_{\text{AS}}$ =55A
- 4. The power dissipation is limited by 150  $^{\circ}\text{C}$  junction temperature
- 5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



# **Typical Characteristics**



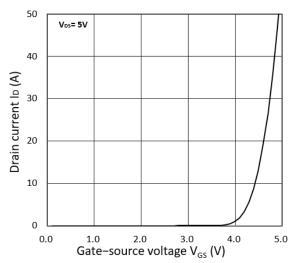


Figure 1. Output Characteristics

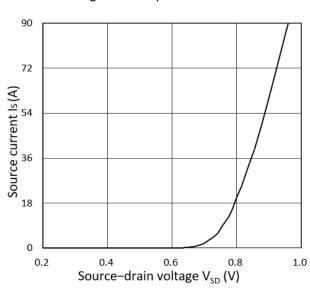


Figure 2. Transfer Characteristics

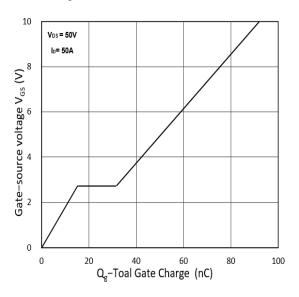


Figure 3. Forward Characteristics of Reverse

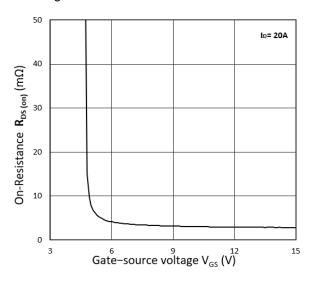


Figure 4. Gate Charge Characteristics

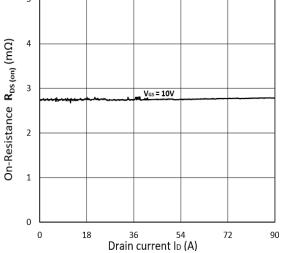
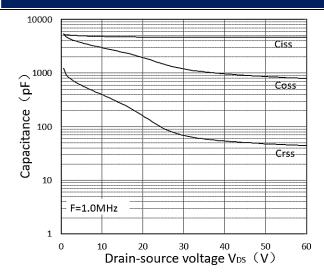


Figure 5.  $R_{DS(on)}$  vs.  $V_{GS}$ 

Figure 6.  $R_{DS(on)}$  vs.  $I_D$ 





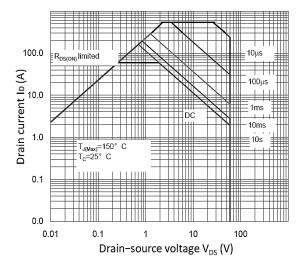


Figure 7. Capacitance Characteristics

Figure 8. Safe Operating Area

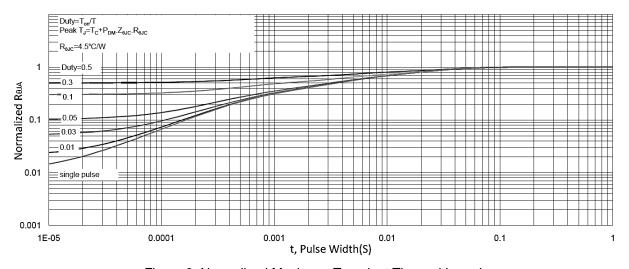
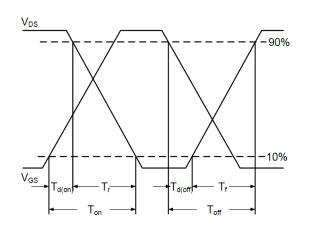


Figure 9. Normalized Maximum Transient Thermal Impedance



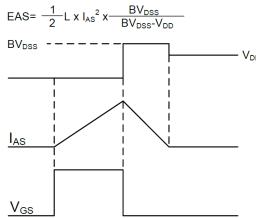


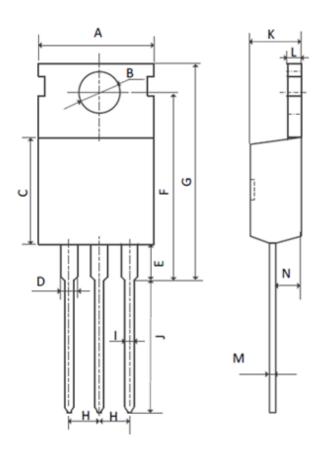
Figure 10. Switching Time Waveform

Figure 11. Unclamped Inductive Switching

Waveform



### **Mechanical Dimensions for TO-220**



### **COMMON DIMENSIONS**

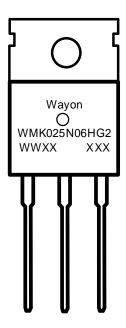
SYMBOL	MM			
	MIN	MAX		
А	9.70	10.30		
В	3.40	3.80		
С	8.80	9.40		
D	1.17	1.47		
Е	2.60	3.50		
F	15.10	16.70		
G	19.55MAX			
Н	2.54REF			
I	0.70	0.95		
J	9.35	11.00		
K	4.30	4.77		
L	1.20	1.45		
М	0.40	0.65		
N	2.20	2.60		



### **Ordering Information**

Part	Package	Marking	Packing method
WMK025N06HG2	TO-220	WMK025N06HG2	Tube

### **Marking Information**



WMK025N06HG2 = Device code WWXX XXX= Date code

#### **Contact Information**

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WAYON website: http://www.way-on.com

For additional information, please contact your local Sales Representative.

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